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PATENT

GP 2881

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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MAY 22 2002

In re application of:

John Jacob Stimson III

Application No.: 09/964,712

Filed: September 27, 2001

For: PARAMETERIZED PREVIEW
ARRAY FOR ITERATIVE IMAGE
OPTIMIZATION IN REMOTE
APPLICATIONS

Group No.: 2881

TC 2800 MAIL ROOM

Examiner: Unknown

**SUBMISSION OF POWER OF
ATTORNEY BY ASSIGNEE**

121 Spear Street, Suite 290
San Francisco, CA 94105

Commissioner for Patents
Washington, DC 20231

Sir:

Enclosed for filing please find a duly executed Power of Attorney by Assignee for the above-identified application which specifies an Assignment recordal date of January 22, 2002, at Reel 12537, Frame 983, and shows Assignee's entire right, title and interest to establish ownership.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Date:

5-7-2002

By:

Michael J. Pollock

Michael J. Pollock (Reg. No. 29,098)

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Commissioner for Patents
Washington, D.C. 20231

Sir:

NATIONAL SEMICONDUCTOR CORPORATION, assignee of the entire right title and interest in the above-identified application by assignment dated November 20, 2001, which assignment was recorded in the Patent and Trademark Office on January 22, 2002, at Reel 12537, Frame 983, hereby appoints the members of the firm of STALLMAN & POLLOCK LLP, a firm composed of:

Michael A. Stallman	29,444	Kathleen A. Frost	37,326
Michael J. Pollock	29,098	Brian J. Keating	39,520

121 Spear Street, Suite 290, San Francisco, CA 94105, (415) 512-1312, FAX (415) 512-1362, and the following members of the National Semiconductor Corporation Intellectual Property Department:

Christopher J. Byrne	32,204	Andrew S. Viger	28,552	Coleman F. Reif	38,593
John Maxin	34,668	Eugene C. Conser	39,149	Allen R. Tremain	40,207
Pater Y. Wang	40,452				

as its attorneys with full power of substitution to prosecute this application and to transact all business in the Patent and Trademark Office in connection therewith.

Please direct all correspondence regarding this application to the following:

STALLMAN & POLLOCK LLP
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Dated: 4/25/02

NATIONAL SEMICONDUCTOR CORPORATION

By: 

Name: Christopher J. Byrne

Title: Director of Intellectual Property and Technology Licensing